

IN THE SPECIFICATION:

Page 9, first full paragraph replace with the following:

In Embodiment 1 of the present invention, there is provided a display device comprising an active matrix substrate having a driver circuit section composed of a plurality of polycrystalline silicon thin film transistors, a counter substrate, a liquid crystal filled between the active matrix substrate and the counter substrate, and ~~an individually-wired line array for supplying electric power or a signal such as a clock signal and a data signal~~ an individually-wired line array for supplying a clock signal, a data signal or electric power to a plurality of circuit elements comprised in the driver circuit section, wherein the individually-wired line array is extended to a peripheral portion of the active matrix substrate, the display device characterized in that: the peripheral portion of the active matrix substrate has an insulator having a via hole and a multi-layer bus line-equipped section having a bus line formed on the insulator, the bus line is connected to the individually-wired line array via the via hole, and the bus line

has a connecting terminal for connecting the display device to an external circuit.

Page 13, replace the last paragraph bridging page 13 and 14 with the following:

i) In Embodiment 3, there is provided a display device comprising an active matrix substrate having a driver circuit section composed of a plurality of polycrystalline silicon thin film transistors, a counter substrate, a liquid crystal filled between the active matrix substrate and the counter substrate, and ~~an individually-wired line array for supplying electric power or a signal such as a clock signal and a data signal~~ an individually-wired line array for supplying a clock signal, a data signal or electric power to a plurality of circuit elements comprised in the driver circuit section, wherein the individually-wired line array is extended to a peripheral portion of the active matrix substrate, the display device characterized in that: the active matrix substrate has a recessed groove formed in the peripheral portion; and a bus line to be connected to the individually-wired line array is buried in the groove.